

Fig. 1

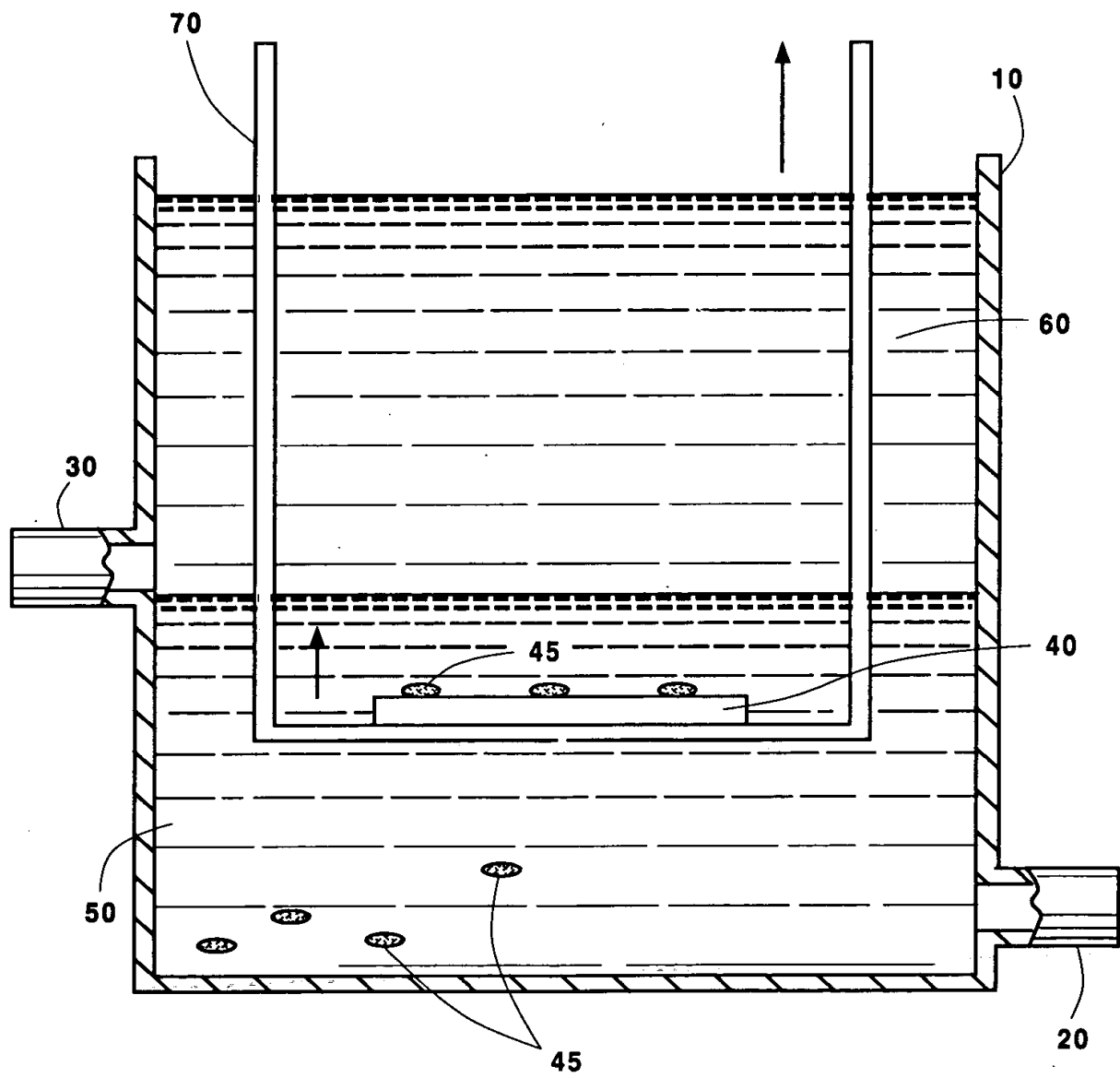


FIG. 2 is a schematic diagram of a device 100 in a cross-sectional view. The device 100 includes a substrate 80, a gate stack 90, a gate 110, a channel 115, a source region 120, a drain region 125, and a contact 130.

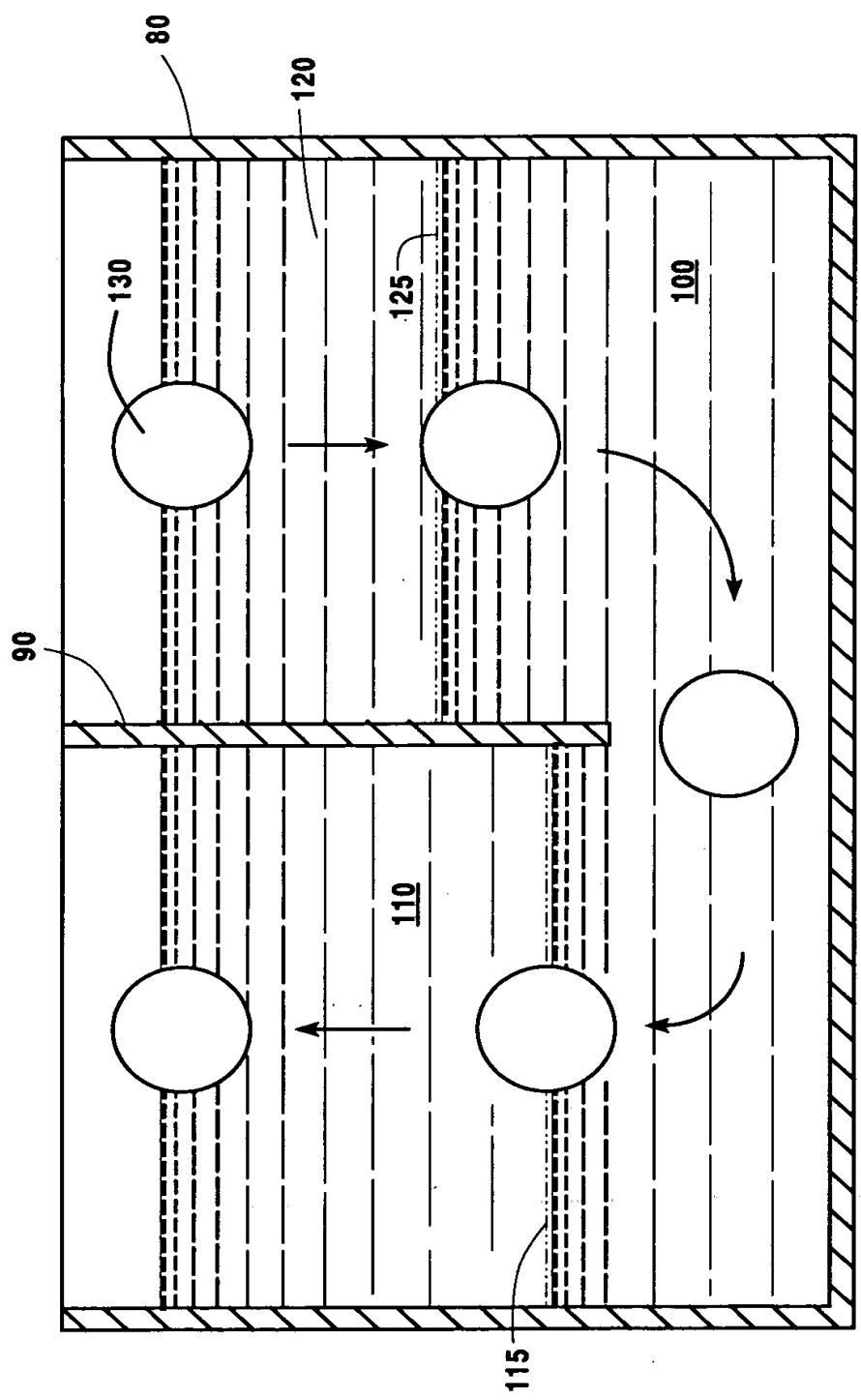


Fig. 2

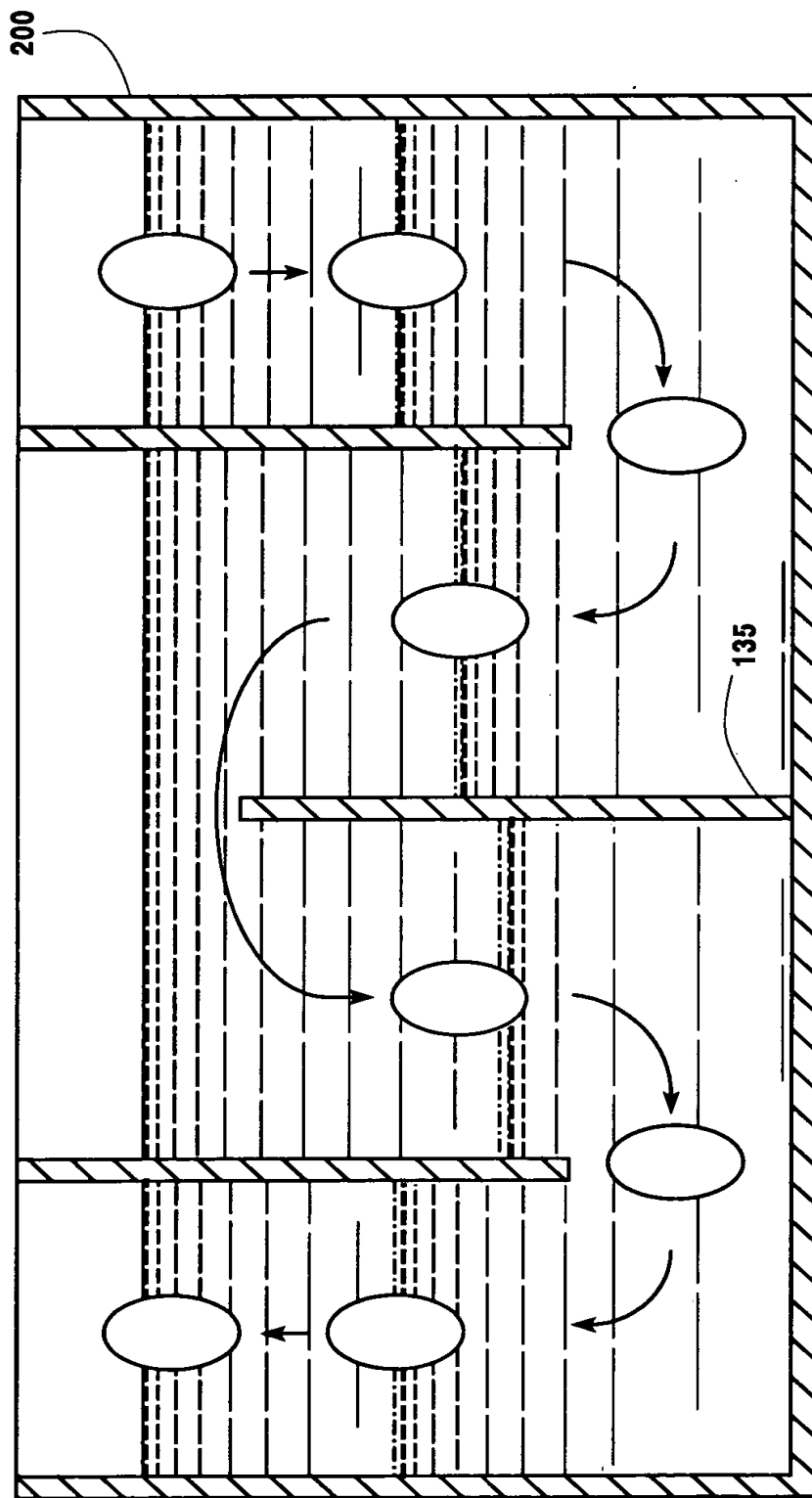


Fig. 3